

FIG. 4A

400 ↗

functionA:

```

402 ~ lv.p v0, 0, 2      !load VLIW 0 with the next 2 instructions
    instr1 } 404
    instr2 } 404
412 ~ lv.p v0, 1, 3      !load VLIW 1 with the next 3 instructions
    instr3 } 414
    instr4 } 414
    instr5 } 414
420 ~ xv.p v0, 0, e = AM !execute VLIW 0, enabling units A and M
422 ~ xv.p v0, 1, e = AMS !execute VLIW 1, enabling units A, M and S
    ret

```

FIG. 4B

450 ↗

functionA':

```

xv.p v0, 0, e = AM      !execute VLIW 0, enabling units A and M
xv.p v0, 1, e = AMS     !execute VLIW 1, enabling units A, M and S
    ret

```

FIG. 5

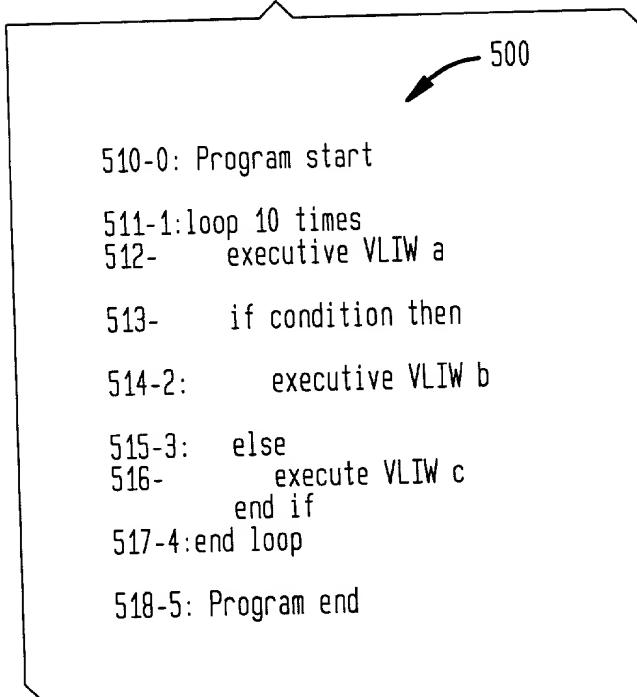
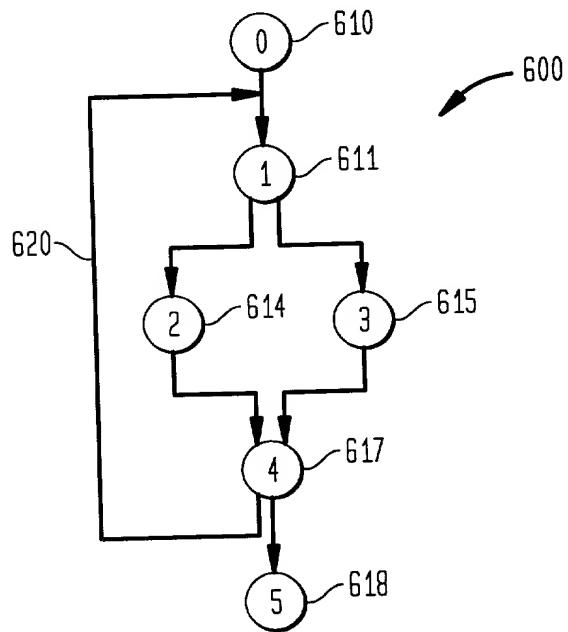


FIG. 6



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FIG. 7

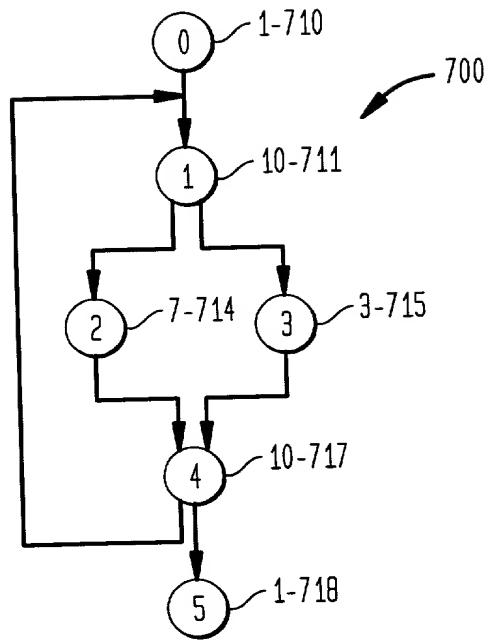


FIG. 8

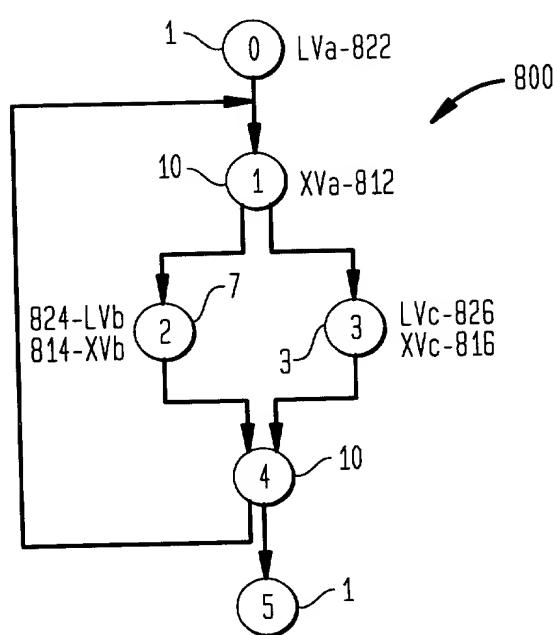


FIG. 9

